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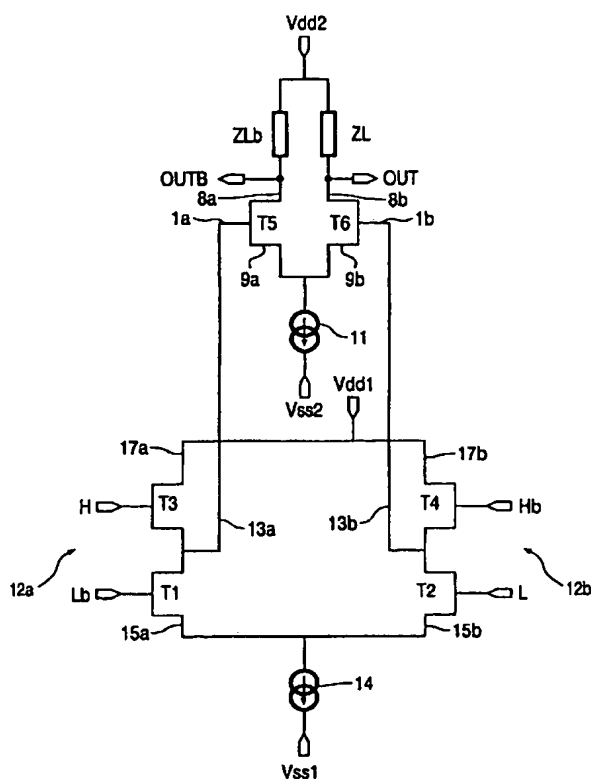
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(54) Title: REGENERATING AMPLIFIER CIRCUIT



(57) Abstract: An amplifier circuit for regenerating complementary digital signals which comprises a differential pair of transistors (T5, T6). This circuit also comprises a pair of push-pull amplifiers (12a, 12b) which consists of a first and a second push-pull amplifier having a first and a second low input (L, Lb) which are coupled to a source for the input signal and its complement, respectively, a first and a second high input (H, Hb) which are coupled to a source of the complementary input signal and to a source of the input signal, respectively, a first and a second output (13a, 13b) which supply, under a low impedance and during switching, brief and intense current pulses of said complementary input signals of increased amplitude in order to apply these pulses to the first and second control inputs (1a, 1b) of the first and second transistors (T5, T6) of the differential pair, respectively.

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